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DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the inventor entitled: "Methods of Forming Integrated Circuitry, Semiconductor Processing Methods, and Processing Method of Forming MRAM Circuitry", the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statement may jeopardize the validity of the application or any patent issued therefrom.

Full name of inventor: Inventor's Signature: Date:	Li Li 1/8/02
Residence:	Meridian, Idaho
Citizenship:	People's Republic of China
Post Office Address:	2790 North Oldstone Way, Meridian, ID 83642
	* * * * * * * * * *
Full name of inventor:	Max F. Hineman
Inventor's Signature:	Mag & Hwenner
Date:	1/8/02
Residence:	Boise, Idaho
Citizenship:	United States of America
Post Office Address:	106 Evergreen Drive, Boise, ID 83716
·	* * * * * * * * * *
Full name of inventor:	Mark E. Tuttle
Inventor's Signature:	May & I
Date:	1-8-02
Residence:	Boise, Idaho
Citizenship:	United States of America

1998 Table Rock Road, Boise, ID 83712

Post Office Address:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No Unknown
Filing Date Filed Herewith
Inventor Li Li et al.
Assignee Micron Technology, Inc.
Group Art Unit Unknown
Examiner Unknown
Attorney's Docket No MI22-1795
Title: Methods of Forming Integrated Circuitry, Semiconductor Processing
Methods, and Processing Method of Forming MRAM Circuitry

POWER OF ATTORNEY BY ASSIGNEE AND CERTIFICATE BY ASSIGNEE UNDER 37 CFR § 3.73(b)

To: Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., the Assignee of the entire right, title and interest in the above-identified patent application by assignment attached hereto, hereby appoints the attorneys and agents of the firm of WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S., listed as follows:

David P. Roberts	Reg.	No.	23,032
Randy A. Gregory	Reg.	No.	30,386
Mark S. Matkin	Reg.	No.	32,268
James L. Price	Reg.	No.	27,376
Deepak Malhotra	Reg.	No.	33,560
Mark W. Hendricksen	Reg.	No.	32,356
David G. Latwesen	Reg.	No.	38,533
George G. Grigel	Reg.	No.	31,166
Keith D. Grzelak	Reg.	No.	37,144
James D. Shaurette	Reg.	No.	39,833
Frederick M. Fliegel	Reg.	No.	36,138
Donald Brent Kenady	Reg.	No.	40,045
James E. Lake	Reg.	No.	44,854
Bernard Berman	Reg.	No.	37,279
Jennifer J. Taylor	Reg.	No.	48,711



and also attorneys Michael L. Lynch (Reg. No. 30,871) and Charles B. Brantley II (Reg. No. 38,086) of Micron Technology, Inc., as its attorneys with full power of substitution to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

The Assignee certifies that the above-identified Assignment has been reviewed and to the best of Assignee's knowledge and belief, title is in the Assignee, and a copy of the Assignment is submitted herewith.

Please direct all correspondence regarding this application to:

Customer No. 021567 Wells, St. John, Roberts, Gregory & Matkin P.S. Attn: Mark S. Matkin 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828

Telephone: (509) 624-4276 Facsimile: (509) 838-3424

MICRON TECHNOLOGY, INC.

Dated: /-14-2002

By:

Name: Michael L. Lynch, Esq.

Title: Chief Patent Counsel

Attachment: Copy of Assignment; Copy of Board of Directors' Resolution



MICRON TECHNOLOGY, INC. CERTIFIED COPY OF RESOLUTIONS

I, Jan R. Reimer, the Assistant Secretary of Micron Technology, Inc. do hereby certify, that the resolutions attached hereto represent a complete, true and correct copy of the resolutions duly adopted by the Board of Directors of Micron Technology, Inc., a corporation duly organized and existing under the laws of the State of Delaware, at a meeting duly held on March 25, 1996, a quorum being present, and have been entered into the minutes of said meeting; that I am the keeper of the corporate seal and of the minutes and records of this Corporation; and that the said resolutions have not been rescinded or modified.

The resolutions attached hereto are in conformity with the Articles of Incorporation and Bylaws of the Corporation and are now in full force and effect.

I further certify that the person whose name and signature is set out below is the person authorized to act for said corporation in transactions with and pursuant to the foregoing resolutions, and that such person is now duly qualified and acting in his respective capacity:

NAME AND TITLE

SIGNATURE

Michael L. Lynch, Assistant General Counsel for Intellectual Property

2027/

IN WITNESS WHEREOF, I have hereunto subscribed my name and affixed the seal of the said corporation, this <u>loth</u> day of <u>Muy</u>, 1996.

Jan R. Reimer, Assistant Secretary

(SEAL)





MICRON TECHNOLOGY, INC. BOARD OF DIRECTORS RESOLUTIONS

WHEREAS, certain key employees require the authority to execute certain documents on behalf of the Company in order to enable them to effectively and efficiently carry out their responsibilities and duties to the Company.

NOW THEREFORE BE IT RESOLVED, that the Board hereby approves and authorizes Mr. Michael L. Lynch, Assistant General Counsel for Intellectual Property, to execute on behalf of the Company, documents pertaining to the Company's patent prosecution matters, including but not limited to documents relating to representation before a patent examining authority, patent terms and other patent prosecution procedures, both in the United States and other countries, upon such terms and conditions as the General Counsel of the Company shall deem necessary or appropriate.